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PATENT NUMBER and ISSUE DATE
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U.S. UTILITY Patent Application

APPL NUM 10083611	FILING DATE 02/27/2002	CLASS 257	SUBCLASS 316	GAU 2812	EXAMINER Boyle
<b>**APPLICANTS:</b> Hisamune Yoshiaki; Nakata Hidetoshi;					
<b>**CONTINUING DATA VERIFIED:</b> THIS APPLICATION IS A DIV OF 09/489,332 01/21/2000 PAT 6,373,096					
<b>BEST AVAILABLE COPY</b>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> JAPAN 11-014707 01/22/1999					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO 8020-1002-1	
TITLE : Method of manufacturing semiconductor device, nonvolatile semiconductor memory device and method of manufacturing the same					
<small>U.S. DEPT. OF COMM./PAT. &amp; TM.-PTO-436L (Rev. 12-94)</small>					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>Assistant Examiner</b>	<b>CLAIMS ALLOWED</b>		
			Total Claims	Print Claim for O.G.	
<b>ISSUE FEE</b>		<b>Primary Examiner</b>	<b>DRAWING</b>		
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.	Print Fig.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	<b>Application Examiner</b>		
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